<table>
<thead>
<tr>
<th>SR. NO.</th>
<th>NAME OF RESOURCE PERSON</th>
<th>BACKGROUND INDUSTRY/ ACADEMIC/ R&amp;D</th>
<th>TOPICS COVERED</th>
<th>DATE</th>
<th>CLASS</th>
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<tbody>
<tr>
<td>1</td>
<td>Mr. Shivanand V. Dixit</td>
<td>Industry, Retd. AGM, BSNL, Belagavi</td>
<td>Developments in Telecommunications</td>
<td>07.04.2017</td>
<td>4th Sem, C-division</td>
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<tr>
<td>2</td>
<td>Mr. Manjunath Shinde</td>
<td>Industry, Doordarshan Goa</td>
<td>HAM radio</td>
<td>4/4/2017</td>
<td>IV sem A</td>
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<tr>
<td>3</td>
<td>Mr. Ravindra Desai</td>
<td>India Labs-Design Center EM Electronics Pvt. Ltd Bangalore</td>
<td>System Design</td>
<td>18/04/2017</td>
<td>4th Sem</td>
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<tr>
<td>6</td>
<td>Mr. Shreyas G. Udupi</td>
<td>Siemens Technology and Services Private Limited, Healthcare Division, Bengaluru</td>
<td>Microprocessors in biomedical applications</td>
<td>15.04.2017</td>
<td>6th Sem B &amp; C Division</td>
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<tr>
<td>7</td>
<td>Mr. Venu Kolathur</td>
<td>Co-Founder and Chief Architect Vayavya Labs Pvt. Ltd</td>
<td>Memory management</td>
<td>18.04.2017</td>
<td>6th Sem C Division</td>
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<tr>
<td>8</td>
<td>Mr. Venu Kolathur</td>
<td>Co-Founder and Chief Architect Vayavya Labs Pvt. Ltd</td>
<td>Virtual Memory and Scheduler Design</td>
<td>19.04.2017</td>
<td>8th Sem A &amp; B Division</td>
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<td>9</td>
<td>Mr. Nitin Bhatti</td>
<td>Tata Docomo</td>
<td>RF Hardware and languages</td>
<td>31 /03/2017</td>
<td>IV -C division</td>
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<td>10</td>
<td>Mr. Nitin Bhatti</td>
<td>Tata Docomo</td>
<td>RF Hardware and languages</td>
<td>31 /03/2017</td>
<td>VI -C division</td>
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<tr>
<td>11</td>
<td>Mr. Prasad Desai</td>
<td>Co-Founder of Sentennial (Product based company)</td>
<td>Opensource and Linux</td>
<td>06/04/17</td>
<td>VI</td>
</tr>
<tr>
<td>12</td>
<td>Mr. Prakash Mugali</td>
<td>Industry</td>
<td>Microwave Heating allcations, Project management, Product development in microwave heating industry</td>
<td>25/04/2017 and 26/04/2017</td>
<td>VII (A, B and C Divisions)</td>
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